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Substitute for form 1449A/PTO				Complete if Known <b>10/16/19/06/07</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	Based on 09768,8301
				Filing Date:	January 25, 2001 <b>8-28-03</b>
				First Named Inventor	Tatsuya ARAO, et al.
				Group Art Unit	2815
				Examiner Name	Edward Wojciechowicz
Sheet	1	of	3	Attorney Docket Number	0756-7188

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
<b>EOJ</b>		5,923,962		Ohtani et al.	07/13/99	
		5,643,826		Ohtani et al.	07/01/97	
		5,247,190		Friend et al.	09/21/93	
		5,399,502		Friend et al.	03/21/95	
		6,278,131		Yamazaki et al.	08/21/01	
		6,088,258		Aitken et al.	07/11/00	
		5,671,027		Sasano et al.	09/23/97	
		6,335,541	B1	Ohtani et al.	01/01/02	
		6,285,042	B1	Ohtani et al.	09/04/01	
		6,348,368	B1	Yamazaki et al.	02/19/02	

FOREIGN PATENT DOCUMENTS							
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<b>EOJ</b>		JP	7-130652			05/19/95	Abst.
		JP	10-092576			04/10/98	Abst.
		PCT	WO90/13148			11/01/90	Eng.
		JP	08-078329			03/22/96	Abst.
		JP	09-312260			12/02/97	Abst.
		JP	10-294280			11/04/97	Abst.

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<b>EOJ</b>		H. Shenk et al., "Polymers for Light-emitting Diodes," Euro Display Proceedings 1999, pp. 33-37.	
		U. S. Patent Application Serial No. 09/487,432, including specification and drawing, "Semiconductor Device and Process for Production Thereof," Shunpei YAMAZAKI, January 19, 2000.	
<b>EOJ</b>		Preliminary Amendment submitted in U.S. Patent Application Serial No. 09/874,670 (now US 2001/0029070) on July 5, 2001.	

Examiner Signature	<b>EOJ</b>	Date Considered	<b>8-1-04</b>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	Based on <b>09/768,830-</b>
				Filing Date	<b>January 25, 2001</b> <b>8-28-03</b>
				First Named Inventor	<b>Tatsuya ARAO, et al.</b>
				Group Art Unit	<b>2815</b>
				Examiner Name	<b>Edward Wojciechowicz</b>
Sheet	<b>2</b>	of	<b>3</b>	Attorney Docket Number	<b>0756-7188</b>

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<b>EA</b>		6,140,667		Yamazaki et al.	10/31/00	
		6,077,731		Yamazaki et al.	06/20/00	
		2001/0029070	A1	Yamazaki et al.	10/11/01	
		5,343,066		Okamoto et al.	08/30/94	
		5,365,079		Kodaira et al.	11/15/94	
		5,608,251		Konuma et al.	03/04/97	
		5,620,905		Konuma et al.	04/15/97	
		6,066,860		Katayama et al.	05/23/00	
		6,147,667		Yamazaki et al.	11/14/00	
<b>EA</b>		6,278,131	B1	Yamazaki et al.	08/21/01	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
<b>EA</b>		JP	03-280018			12/11/91	Full
		JP	04-219736			08/10/92	Full
		JP	05-034718			02/12/93	Full
<b>EA</b>		JP	11-191628			07/13/99	Abst.

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<b>EA</b>		U. S. Patent Application Serial No. 09/479,842, including specification, drawings, and filing receipt, "Semiconductor Device and Method of Fabricating the Same," Shunpei YAMAZAKI, et al., January 10, 2000.	
		Ohtani et al., "LP-B: Late-News Poster: A 60-in. HDTV Rear-Projector with Continuous-Grain-Silicon Technology," May 17-22, 1998, pp. 467-470, SID 98 Digest, International Symposium Digest of Technical Papers, Vol. XXIX.	
<b>EA</b>		Shimokawa et al., "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, Vol. 27, No. 5, May 1988, pp. 751-758.	

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EO		6,166,414		Miyazaki et al.	12/26/00	
		2002-0175376	A1	Ohtani et al.	11/28/02	
		6,465,268	B2	Hirakata et al.	10/15/02	
		6,356,330	B1	Ando et al.	03/12/02	
		5,739,882		Shimizu et al.	04/14/98	

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
EO		JP	06-148685			05/27/94	Full
		JP	07-235680			09/05/95	Full
		JP	08-274336			10/18/96	Full
		JP	2000-183356			06/30/00	Abst.
		JP	06-260645			09/16/94	Full
		JP	06-013407			01/21/94	Full
		EP	1 001 467	A2		05/17/00	Eng.
EO		EP	1 005 094	A2		05/31/00	Eng.
		JP	11-095194			04/09/99	Abst.

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EO		U. S. Patent Application Serial No. 09/714,891, including specification, drawings, "Semiconductor Device with Tapered Gate and Insulating Film," Koji ONO, et al., November 17, 2000.	
EO		U. S. Patent Application Serial No. 09/477,865, including specification, drawings, "Semiconductor Device and Manufacturing Method Thereof,"	

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